

FIG. 1 is a block diagram of a system 10. The system 10 includes an external computer 14, an external display 32, a device 12, and an antenna 34. The external computer 14 is connected to the external display 32 and the device 12 via a connection 36. The device 12 includes a device main processor 26, a device display 20, a secure display 24, a secure module 28, a device keyboard 22, and an external button 30. The device main processor 26 is connected to the device display 20, the secure display 24, the secure module 28, the device keyboard 22, and the external button 30. The secure module 28 is connected to the device main processor 26 and the external button 30. The antenna 34 is connected to the device main processor 26.

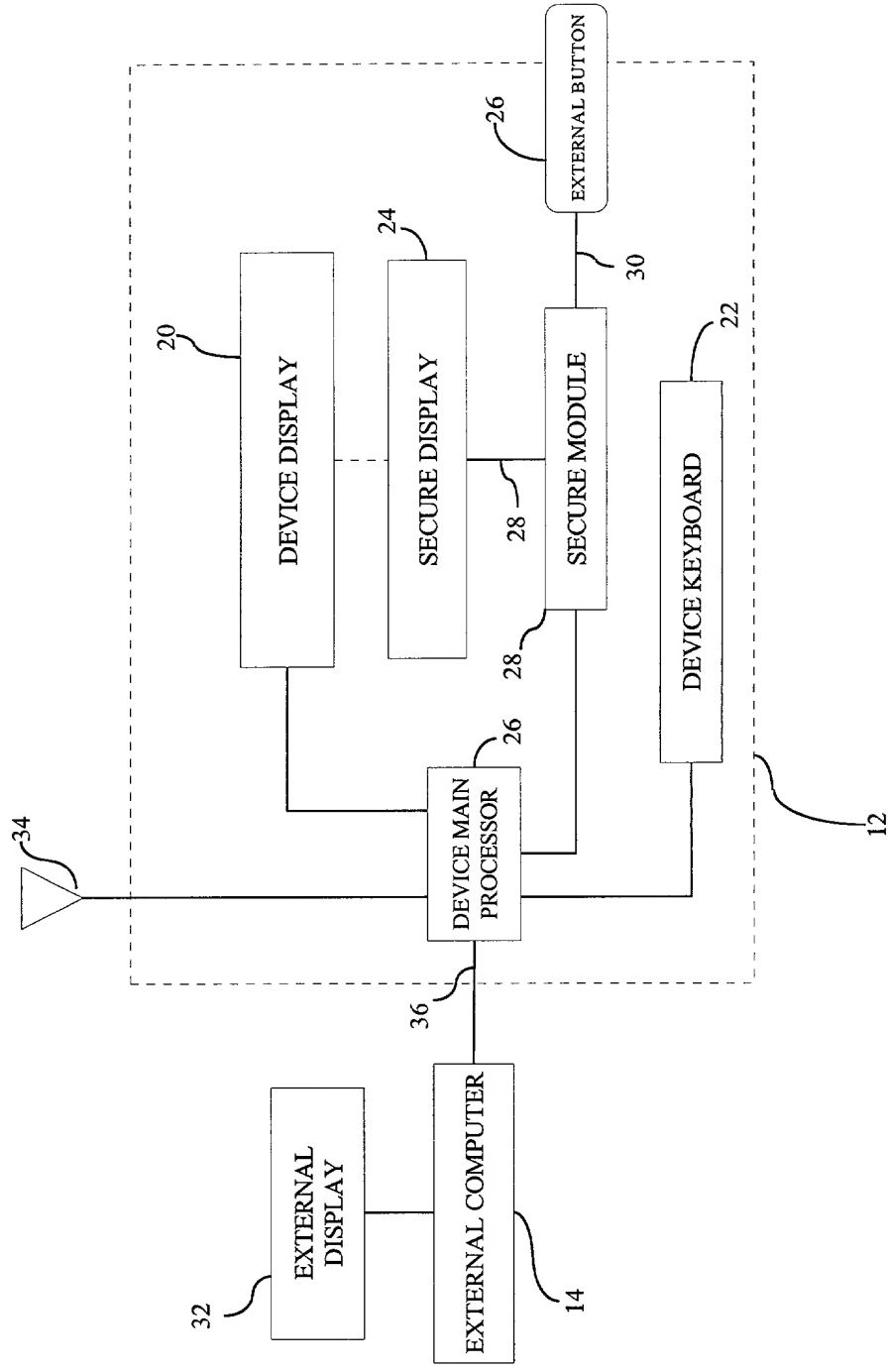


Fig. 1

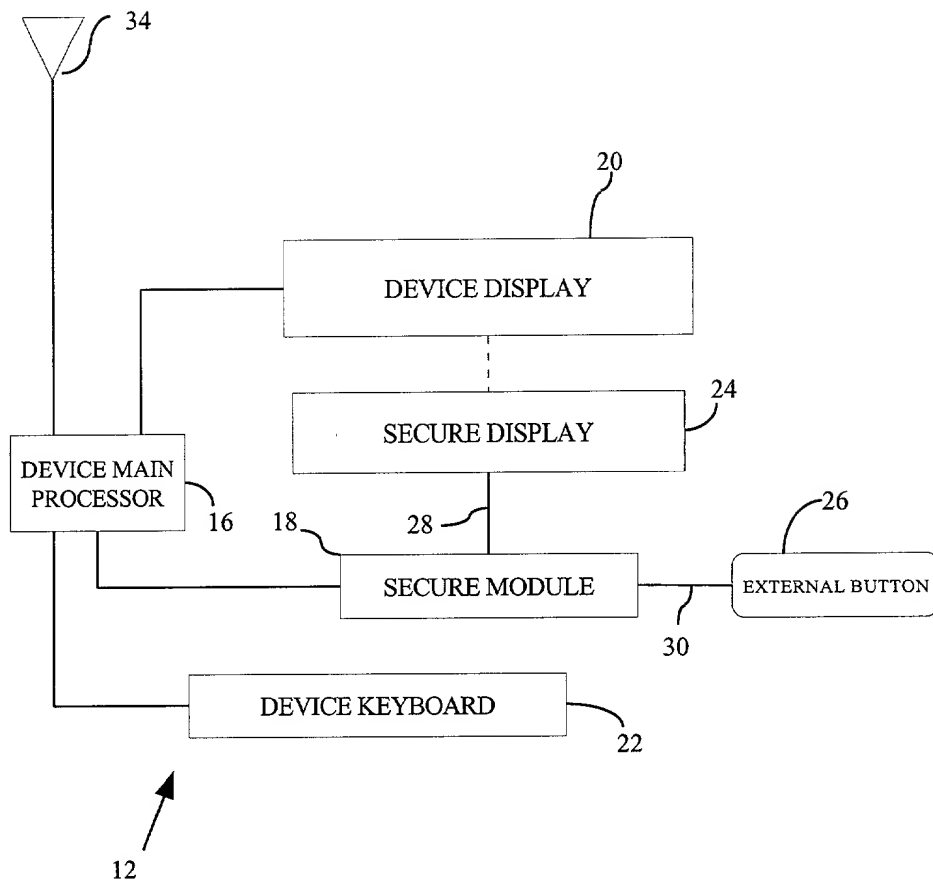


Fig. 2

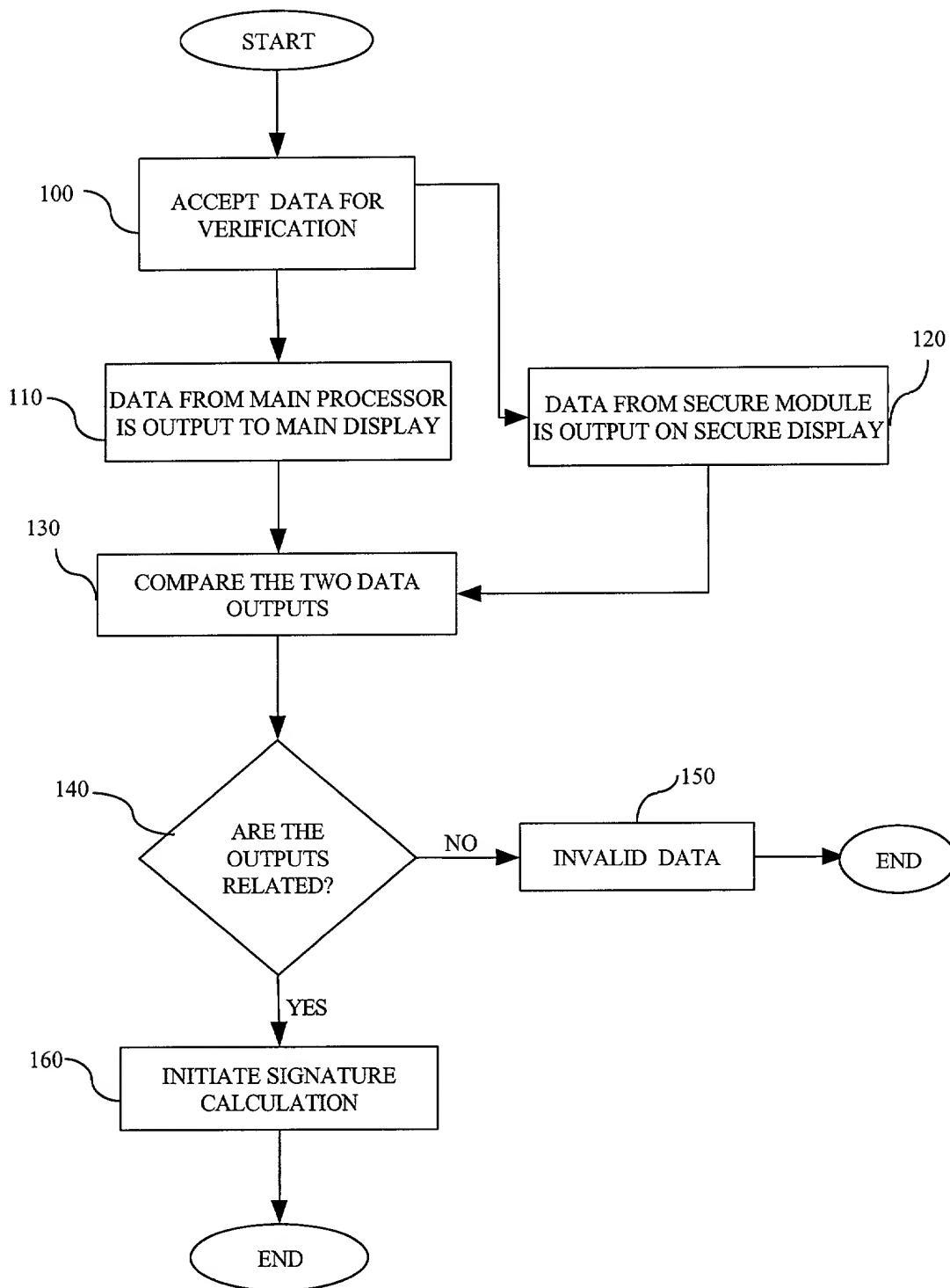


Fig. 3